CS4223 Assignment 2

# Introduction

**Goal**: Simulate uniprocessor cache and multiprocessor with MSI and MESI cache coherence protocols.

**Simulation Environment:**

1. Java Programming Language, Version 1.8
2. Eclipse IDE

**Assumptions made:**

1. A cache waiting for a flush gets the data only after 10 cycles
2. Each cache only has 1 pending instruction in the Bus Request Queue
3. Cache hit includes cases where the cache block was found but a bus transaction still needs to be generated

# Implementation

//some stuff about the basic classes

* 1. Design

//about classes, flowcharts, how we calculate the cycles, bus traffic, etc.

## Results

//results for various files and various sizes of cache, no.processors, etc.

# Conclusion

//challenges and limitations faced

//conclude